IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: SANCHES ET AL.

Serial No. 09/915,761

Filing Date: July 26, 2001

For: DIGITAL SIGNAL PROCESSOR WITH PARALLEL ARCHITECTURE

CITATION UNDER 37 CFR §1.97

Director, U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR \$1.97 and Section 609 of the MPEP.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, D.C. 20231, on this ______ day of September, 2001.

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Case No. 00GR01954248



GAU 2183

DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE WASHINGTON, DC 20231

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ARCHITECTURE

Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

- [X] This IDS is submitted under 37 C.F.R. § 1.97. No fee is required. 1.
- This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the 2. amount of \$ 180.00.
- This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required. 3.
- This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check 4. in the amount of \$130.00 to cover the petition fee.
- The Commissioner is hereby authorized to charge or credit any 5. [X] discrepancies in fee amounts to Deposit Account No. 01-0484.

Date: September 17, 2001

MICHAEL W. TAYLO

Reg. No. 43,182